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PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Masamoto TAGO et al.

Serial No. 09/613,331

GROUP 2818

Filed July 7, 2000

Examiner Q. Hoang

SYSTEM SEMICONDUCTOR DEVICE AND  
METHOD OF MANUFACTURING THE SAME

AMENDMENT

Commissioner for Patents

Washington, D.C. 20231

Sir:

Responsive to the Official Action of January 9, 2002,  
please amend the above-identified application as follows:

IN THE CLAIMS:

Amend claim 1 as follows:

*B17*  
--1. (amended) A system semiconductor device,  
comprising:

a system LSI cell portion which includes a plurality of functional blocks for realizing specific functions, each of the functional blocks serving as a unit circuit and being arranged on a semiconductor chip; and

a global wiring layer which has a wiring layer on a semiconductor substrate and which is laminated with the system LSI cell portion such that the functional blocks are electrically connected to each other;

wherein:

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